

**Ihsan Erdin** is the Engineering Design Manager and SI/PI subject matter expert at Celestica. His primary job function is Celestica's global SI/PI/EMC subject matter expert on product development and technical services. The responsibilities also include mentoring and providing technical support to the SI/PI design and analysis team. He has 20 years of experience in signal and power integrity design of multi-gigabit rate ATCA core and edge routers, including Metro Ethernet, ATM, MPLS and Carrier Ethernet line cards/ backplanes. He is an expert in timing, jitter and power/signal integrity analysis of high-speed serial and parallel interfaces as well as power and still-air thermal analysis of printed circuit structures with an in-depth knowledge of electromagnetic theory, expert on transmission lines, microwave/EMI filters as well as grounding techniques for challenging mixed-analog/digital systems. He has published over 30 journal and conference papers on electromagnetic analysis of printed circuit structures with particular emphasis on signal and power integrity. Dr. Erdin has authored over 150 signal and power integrity reports for a variety of customers including IBM, Microsoft, CISCO, Ciena, DirecTV, NEC, Juniper and others